

*Architecture For High Throughput Semiconductor
Processing Applications*
Inventors: Craig L. Stevens, et al.
Serial #: 09/872,796

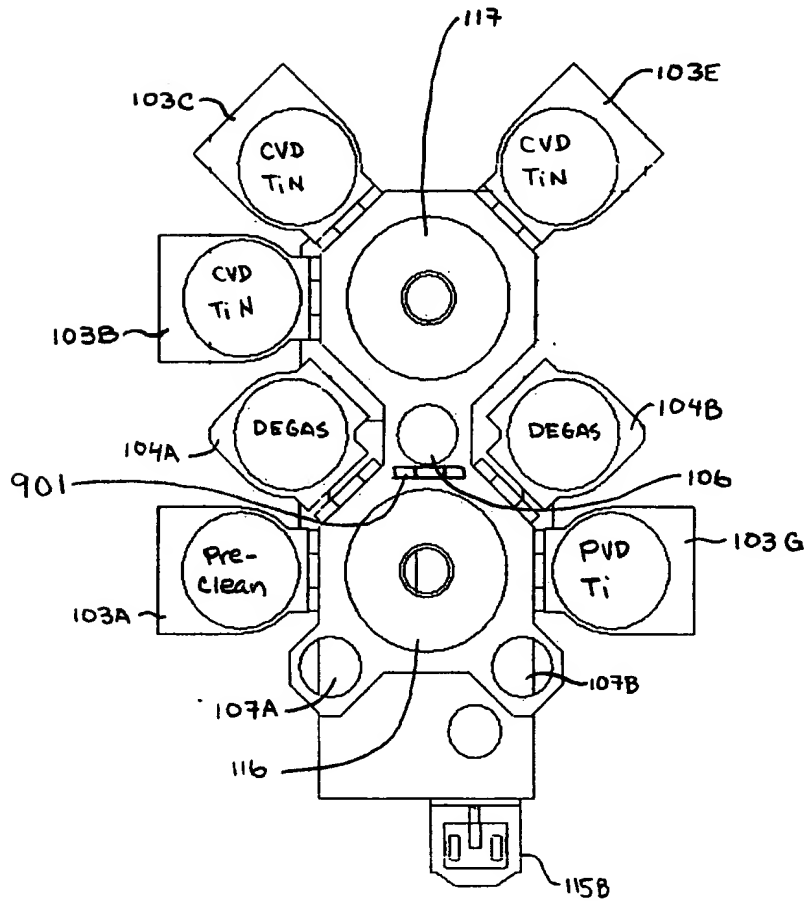


FIG. 10B

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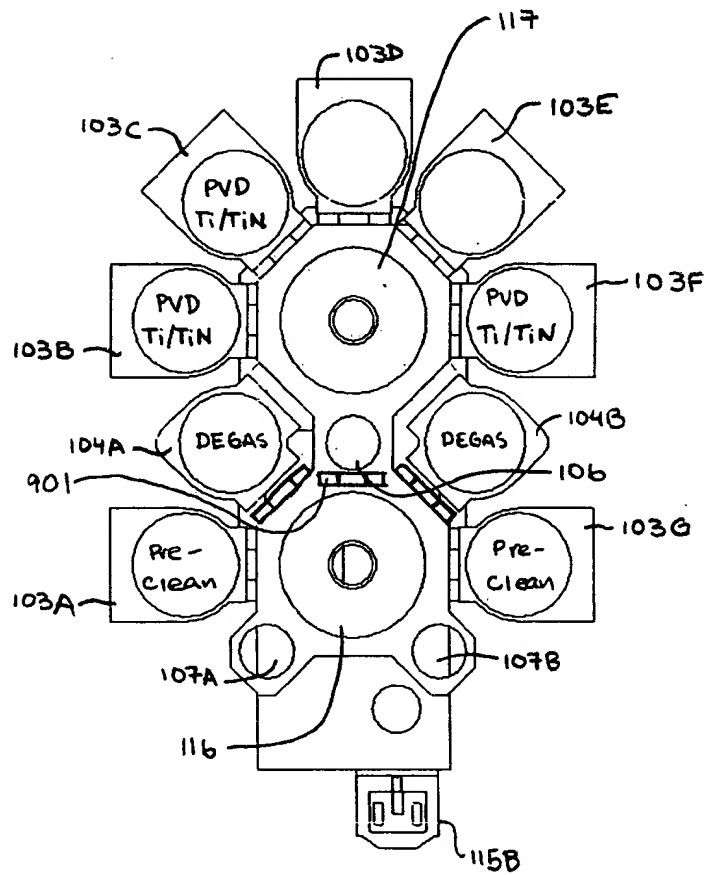


FIG. 10A

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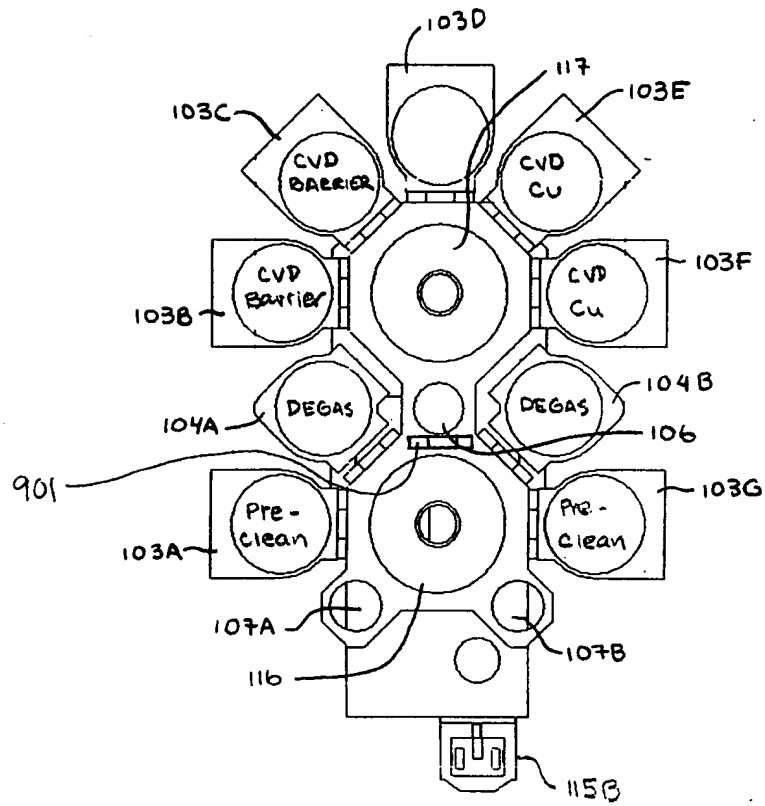


FIG. 9D

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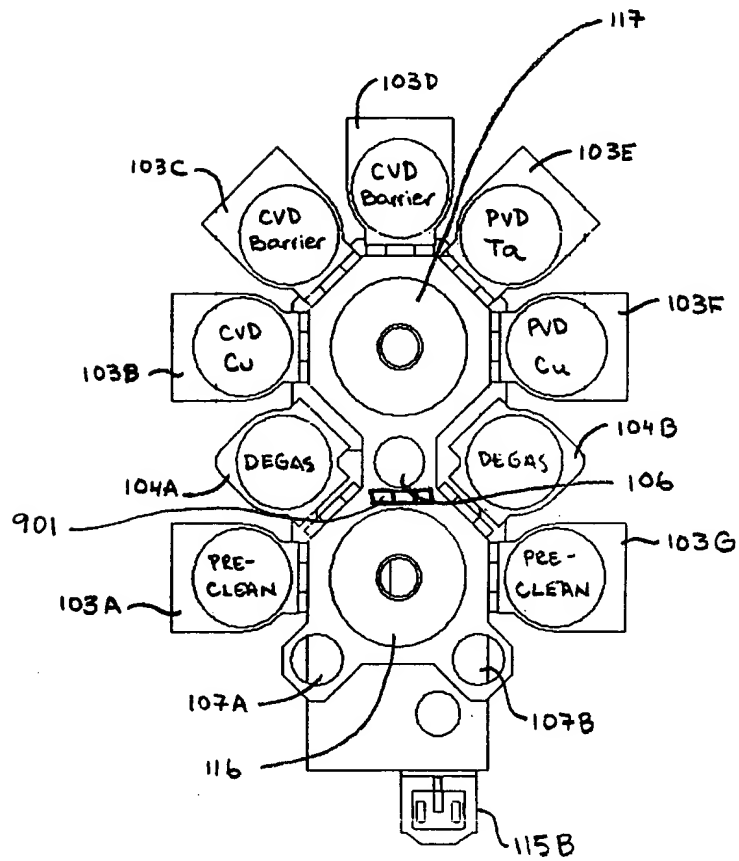


FIG. 9C

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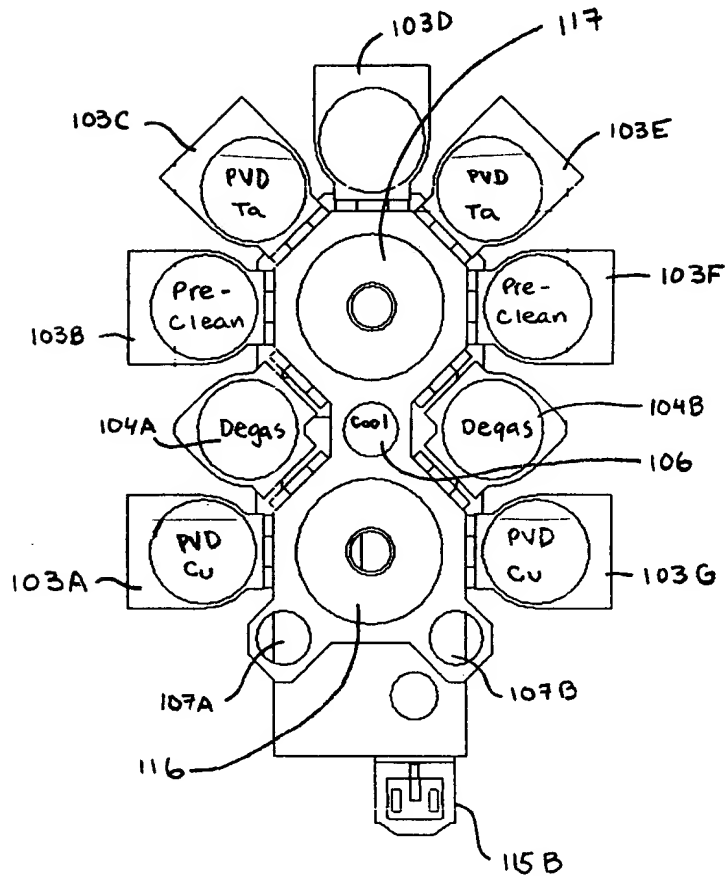


FIG. 9A

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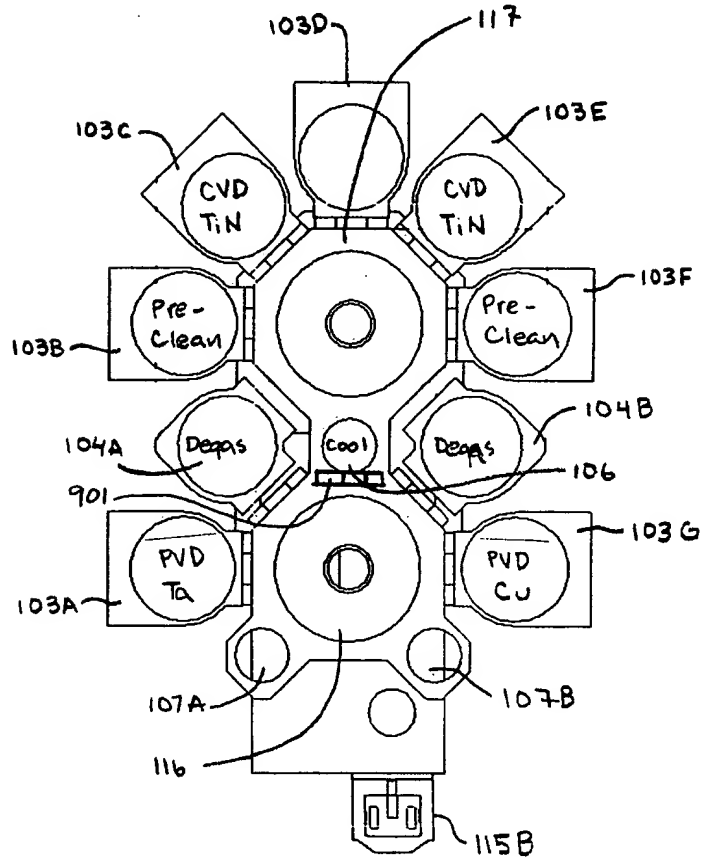
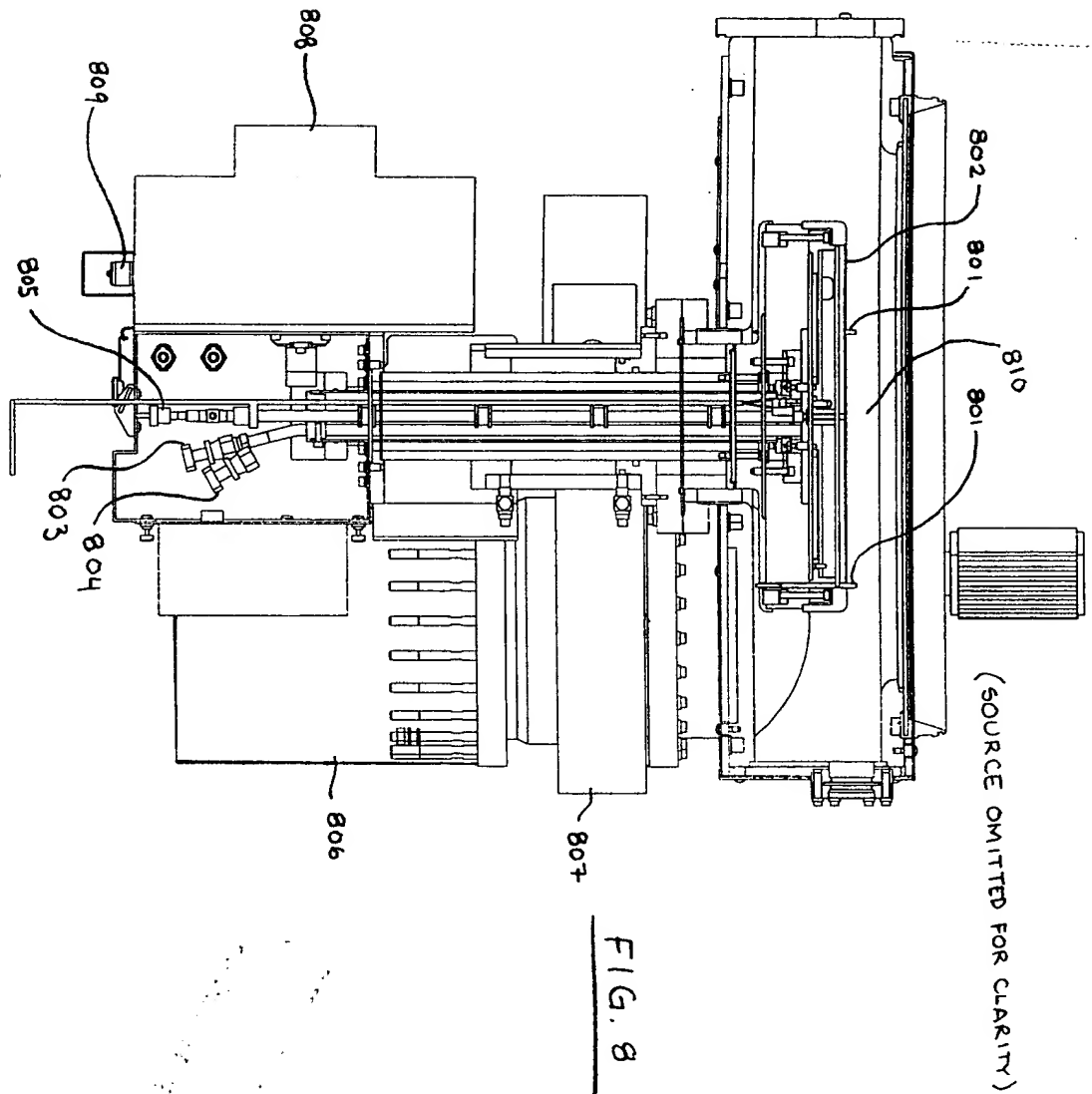
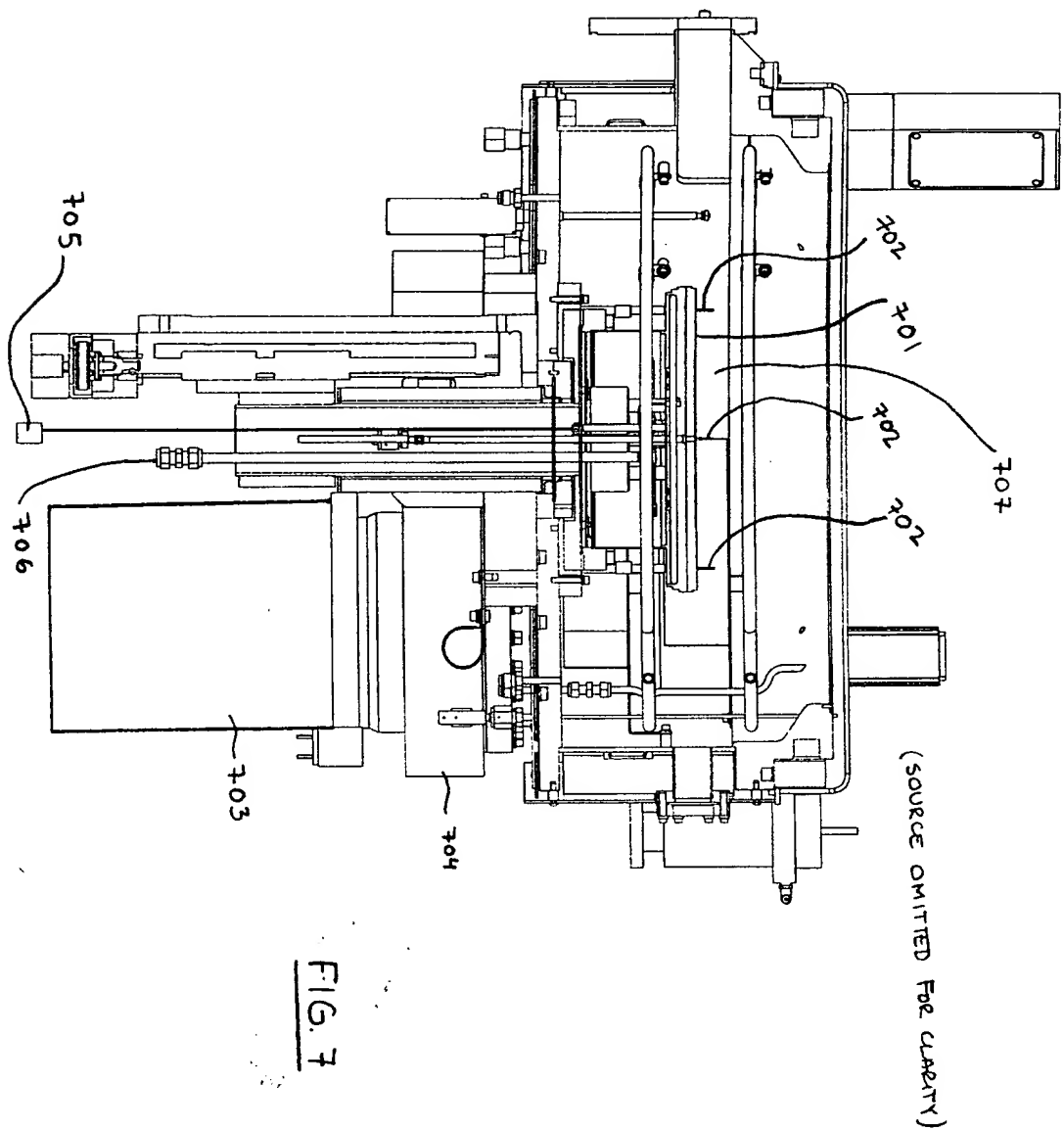


FIG. 9B

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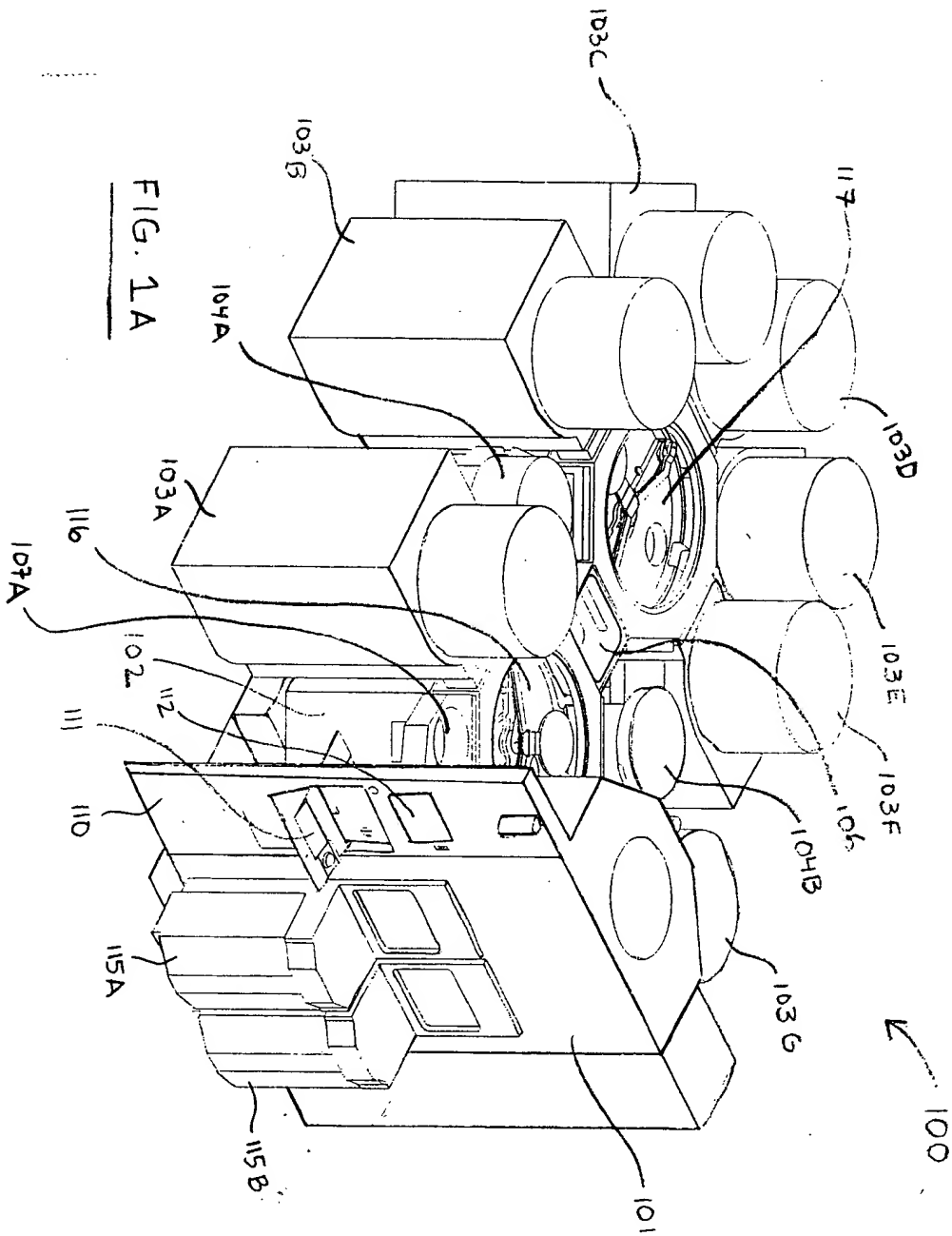
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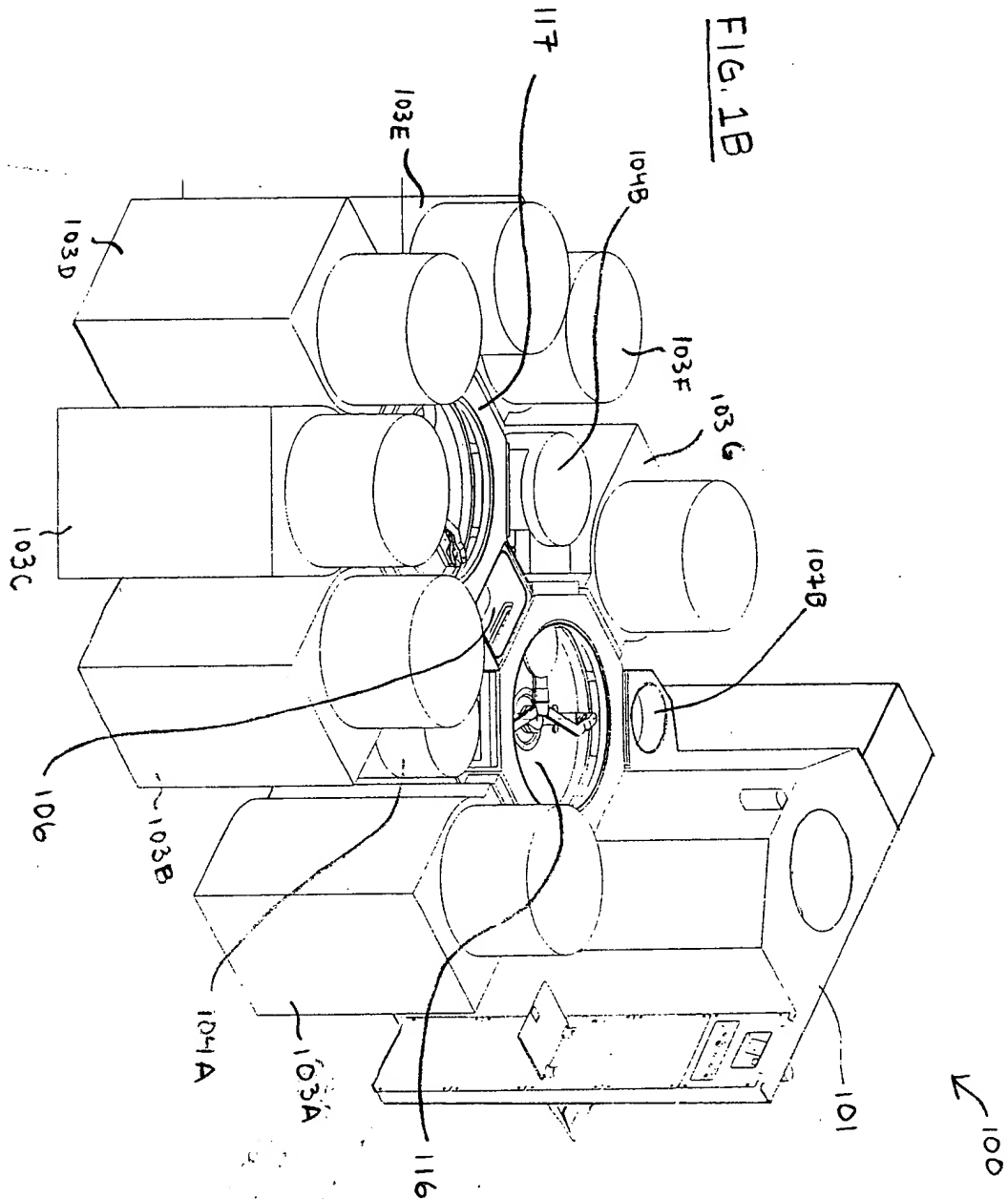
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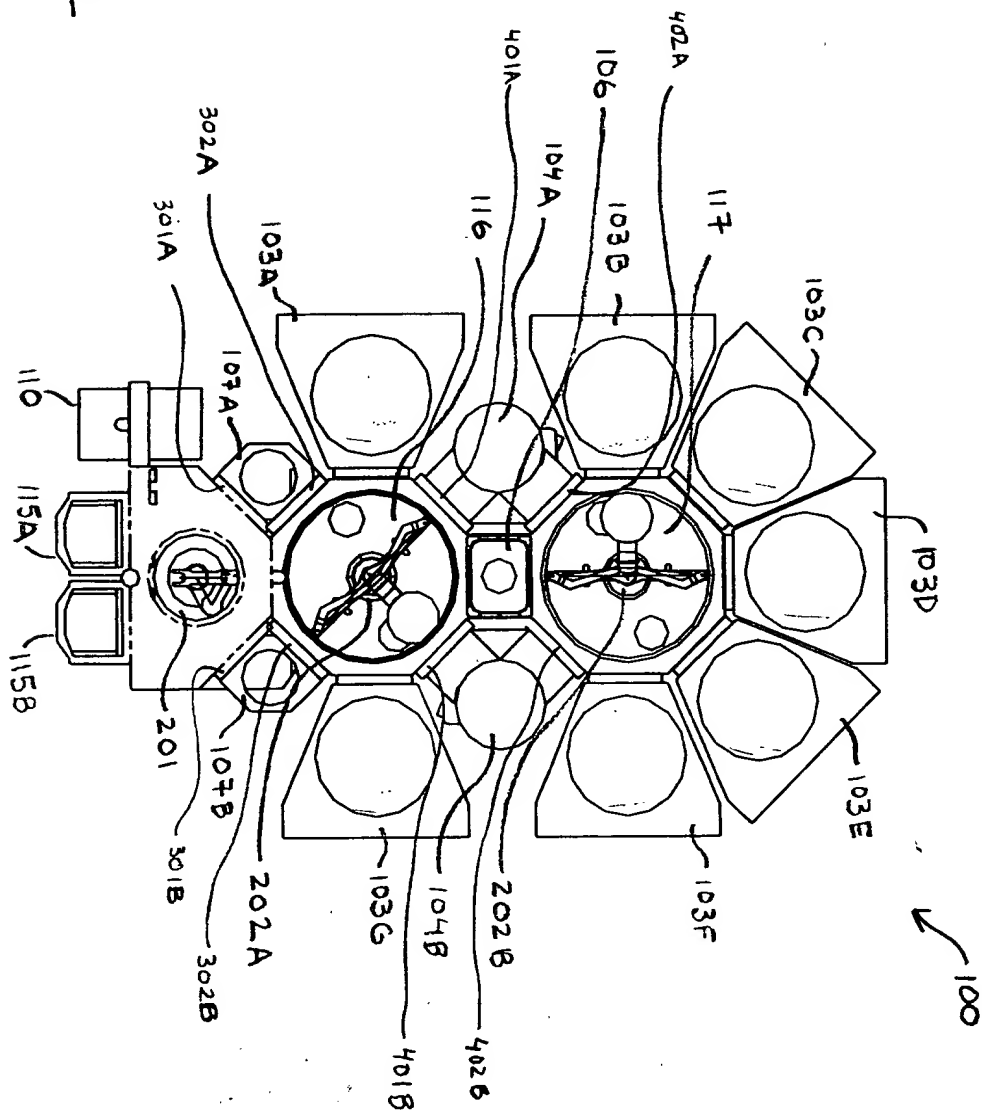


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FIG. 2



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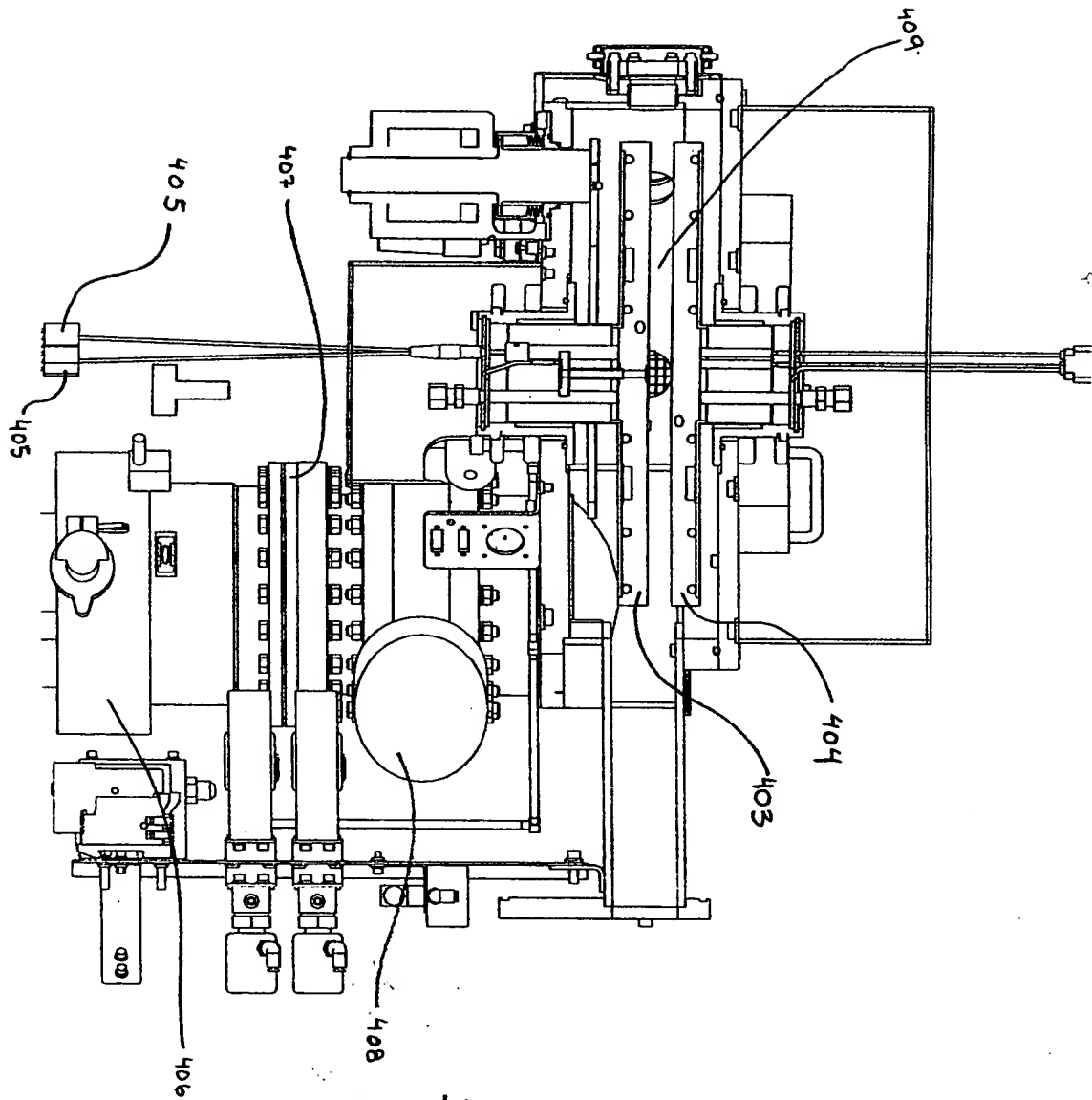


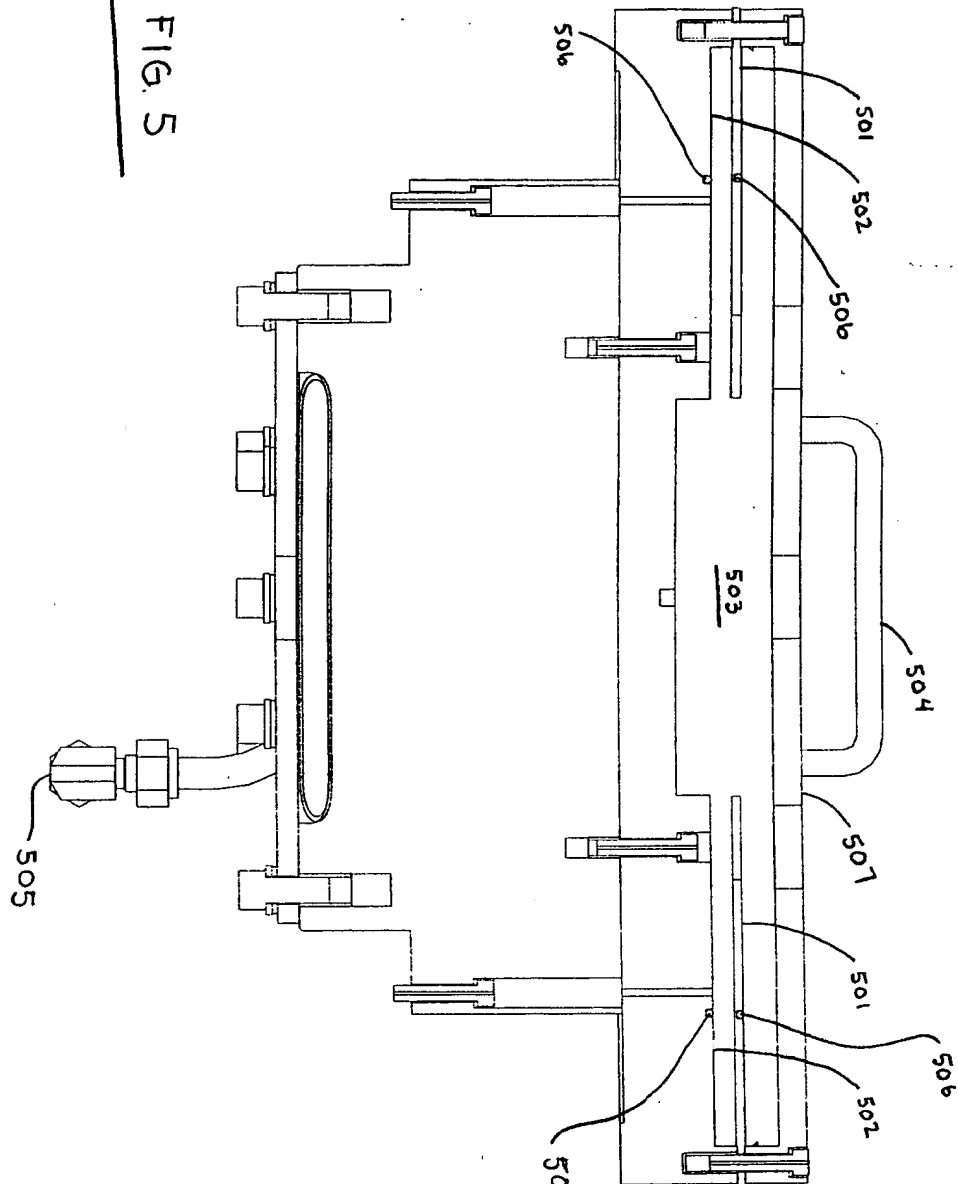
FIG. 4

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F16.5



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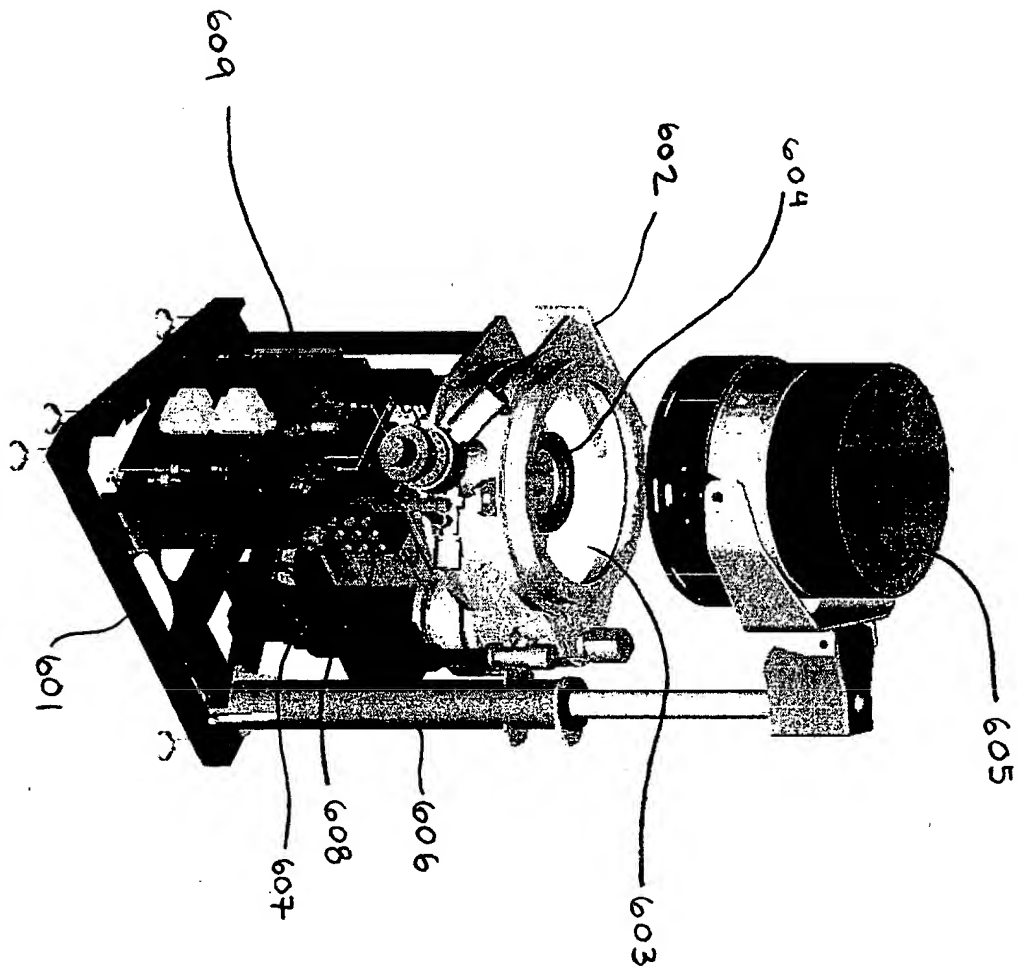


FIG. 6